

U.S. Department of Commerce, Patent and Trademark Office <div style="display: flex; align-items: center;"> <div style="border: 1px solid black; border-radius: 50%; padding: 10px; text-align: center; margin-right: 10px;"> FOR RELEVANT ART CITED BY APPLICANT JUN 28 2004 PATENT & TRADEMARK OFFICE </div> <div> (Write several sheets if necessary) </div> </div>				Docket No.: BUR920030023US1		Serial No.: 10/605,591	
Applicants: Clarence R. Ogilvie et al.				Filing Date: October 10, 2003		Group: _____	

U.S. Patent Documents							
*Examiner Initial	Document Number	Issue Date	Name	Class	Subclass	Filing Date If Appropriate	
JMP	US-1	4,729,090	03/01/88	Baba	710	107	
J	US-2	5,218,680	06/08/93	Farrell et al.	709	215	
J	US-3	5,218,686	06/08/93	Thayer	711	100	
J	US-4	5,274,795	12/28/93	Vachon	710	306	
	US-5						
	US-6						

Foreign Patent Documents							Translation	
	Document Number	Date	Country	Class	Subclass	Yes	No	
JMP	F-1	JP 61123969A	06/11/86	Japan		X abstract only		
JMP	F-2	JP 60160459A	08/22/85	Japan		X abstract only		
JMP	F-3	JP 61143863A	07/01/86	Japan		X abstract only		
	F-4							
	F-5							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
JMP	OT-1 United States Patent Application Pub. No.: US 2002/0129184 A1, Pub. Date: September 12, 2002, <i>Watanabe</i>
J	OT-2 K. Hwang, et al., "OMP: A RISC-based Multiprocessor using Orthogonal-Access Memories and Multiple Spanning Buses*", Laboratory for Parallel and Distributed Computing, University of Southern California, Los Angeles, CA 90089 1990, pps. 7-22
J	OT-3 Ewert et al., "Optimizing Software Performance for IP Frame Reassembly in an Integrated Architecture", WOSP 2000, Ontario, Canada, pps. 29-37
J	OT-4 D.J. Schuelka, IBM Technical Bulletin, "Master/Slave Cascade Channel for Microprocessor DMA", IBM Corp. 1979, Vol. 22, No. 5, pps. 2041-2042
J	OT-5 IBM Technical Bulletin, "Multiword Direct Memory Access Integrated Drive Electronics Hogpen", Vol. 39, No. 04, April 1996, pps. 203-206

Examiner: <i>Sam Poy</i>	Date Considered: <i>8/5/05</i>
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.